

# MOS FIELD EFFECT TRANSISTORS 2SK2367/2SK2368

# SWITCHING N-CHANNEL POWER MOS FET INDUSTRIAL USE

#### **DESCRIPTION**

The 2SK2367/2SK2368 is N-Channel MOS Field Effect Transistor designed for high voltage switching applications.

#### **FEATURES**

· Low On-Resistance

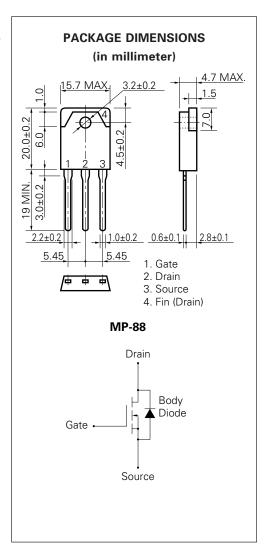
2SK2367: RDS (on) = 0.5  $\Omega$  (VGS = 10 V, ID = 8.0 A) 2SK2368: RDS (on) = 0.6  $\Omega$  (VGS = 10 V, ID = 8.0 A)

- Low Ciss Ciss = 1 600 pF TYP.
- High Avalanche Capability Ratings

#### ABSOLUTE MAXIMUM RATINGS (TA = 25 °C)

Drain to Source Voltage (2SK2367/2SK2368)	VDSS	450/500	V
Gate to Source Voltage	Vgss	±30	V
Drain Current (DC)	ID (DC)	±15	Α
Drain Current (pulse)*	ID (pulse)	±60	Α
Total Power Dissipation ( $T_c = 25$ °C)	P <sub>T1</sub>	120	W
Total Power Dissipation (T <sub>A</sub> = 25 °C)	P <sub>T2</sub>	3.0	W
Channel Temperature	Tch	150	°C
Storage Temperature	T <sub>stg</sub> –	55 to +150	°C
Single Avalanche Current**	las	15	Α
Single Avalanche Energy**	Eas	161	mJ

- \* PW  $\leq$  10  $\mu$ s, Duty Cycle  $\leq$  1 %
- \*\* Starting T<sub>ch</sub> = 25 °C, R<sub>G</sub> = 25  $\Omega$ , V<sub>GS</sub> = 20 V  $\rightarrow$  0



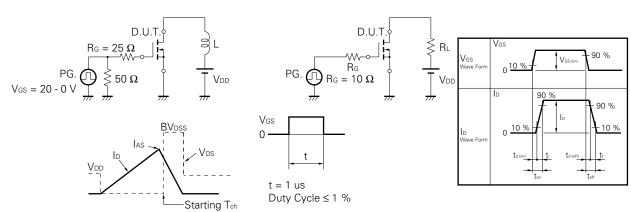


# ELECTRICAL CHARACTERISTICS (TA = 25 °C)

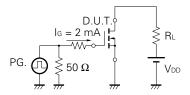
CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS	
Drain to Source On-Resistance	RDS (on)		0.4	0.5	Ω	Vgs = 10 V	2SK2367
			0.5	0.6		ID = 8.0 A	2SK2368
Gate to Source Cutoff Voltage	VGS (off)	2.5		3.5	V	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	
Forward Transfer Admittance	l yfs l	5.0			S	VDS = 10 V, ID = 8.0 A	
Drain Leakage Current	IDSS			100	μΑ	V <sub>DS</sub> = V <sub>DSS</sub> , V <sub>GS</sub> = 0	
Gate to Source Leakage Current	Igss			±100	nA	Vgs = ±30 V, Vps = 0	
Input Capacitance	Ciss		1 600		pF	Vps = 10 V	
Output Capacitance	Coss		300		pF	V <sub>GS</sub> = 0	
Reverse Transfer Capacitance	Crss		30		pF	f = 1 MHz	
Turn-On Delay Time	td (on)		30		ns	ID = 8.0 A	
Rise Time	tr		40		ns	Vgs = 10 V	
Turn-Off Delay Time	td (off)		70		ns	V <sub>DD</sub> = 150 V	
Fall Time	<b>t</b> f		25		ns	$R_G = 10 \Omega R_L = 18.8 \Omega$	
Total Gate Charge	QG		43		nC	ID = 15 A	
Gate to Source Charge	Qgs		10		nC	V <sub>DD</sub> = 400 V	
Gate to Drain Charge	Q <sub>GD</sub>		20		nC	V <sub>GS</sub> = 10 V	
Body Diode Forward Voltage	VF (S-D)		1.0		V	IF = 15 A, VG	s = 0
Reverse Recovery Time	trr		400		ns	IF = 15 A, VG	s = 0
Reverse Recovery Charge	Qrr		1.8		μC	di/dt = 50 A/	μs

# **Test Circuit 1 Avalanche Capability**

# **Test Circuit 2 Switching Time**

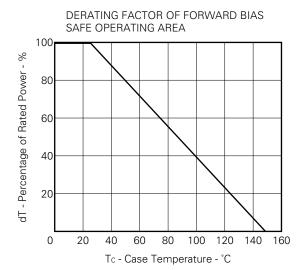


### **Test Circuit 3 Gate Charge**

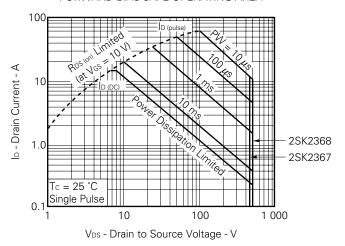


The application circuits and their parameters are for references only and are not intended for use in actual design-in's.

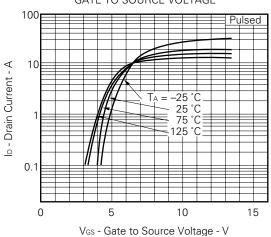
#### TYPICAL CHARACTERISTICS (TA = 25 °C)

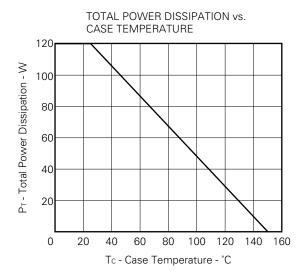


#### FORWARD BIAS SAFE OPERATING AREA

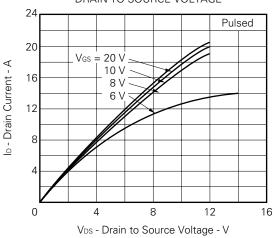


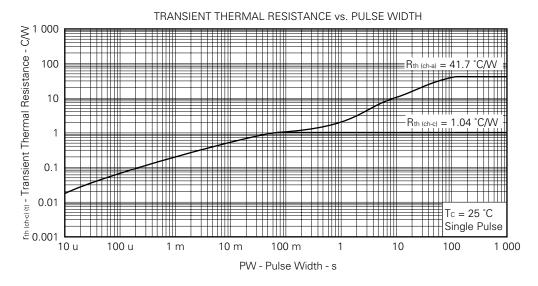
DRAIN CURRENT vs. GATE TO SOURCE VOLTAGE



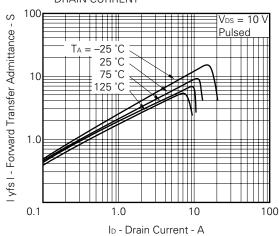


DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE

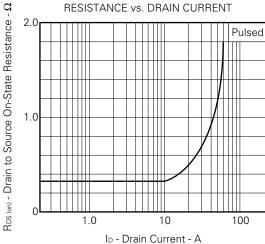




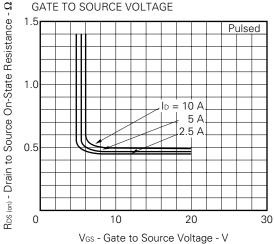




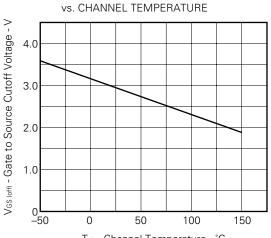


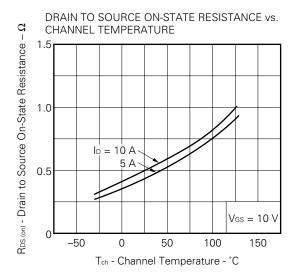


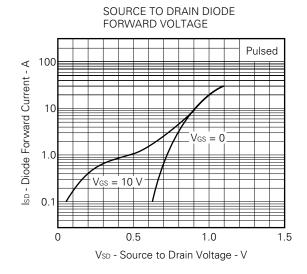
#### DRAIN TO SOURCE ON-STATE RESISTANCE vs. **GATE TO SOURCE VOLTAGE**

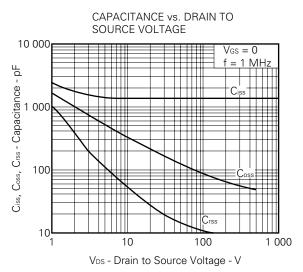


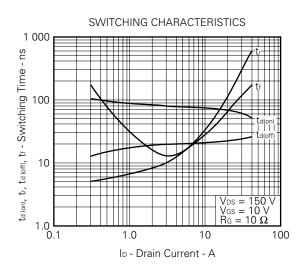
# GATE TO SOURCE CUTOFF VOLTAGE

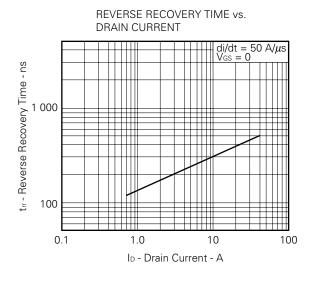


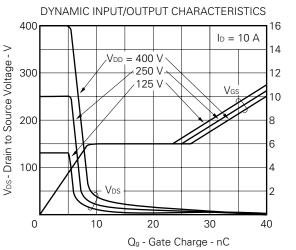






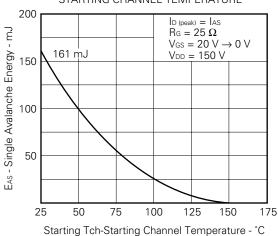




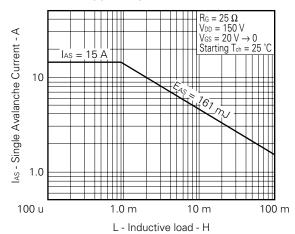




# SINGLE AVALANCHE ENERGY vs. STARTING CHANNEL TEMPERATURE



# SINGLE AVALANCHE CURRENT vs. INDUCTIVE LOAD





#### **REFERENCE**

Document Name	Document No.
NEC semiconductor device reliability/quality control system.	TEI-1202
Quality grade on NEC semiconductor devices.	IEI-1209
Semiconductor device mounting technology manual.	IEI-1207
Semiconductor device package manual.	IEI-1213
Guide to quality assurance for semiconductor devices.	MEI-1202
Semiconductor selection guide.	MF-1134
Power MOS FET features and application switching power supply.	TEA-1034
Application circuits using Power MOS FET.	TEA-1035
Safe operating area of Power MOS FET.	TEA-1037

The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device is actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

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